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Substitute for form 1449A/PTO
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known

Application Number	09/135,413
Filing Date	August 14, 1998
First Named Inventor	Forbes, Leonard
Group Art Unit	2818 2827
Examiner Name	Nguyen, Viet

Sheet 1 of 1

Attorney Docket No: 303.354US2

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
V	US-6,049,091	04/11/2000	Yokoyama, T.	257	52	06/30/1997

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
V		MADELUNG, O. , "Semiconductors-Basic Data", Springer-Verlag, Second Revised Edition, ISBN: 3-540-53150-5,(1996),49-53	

EXAMINER

V. Nguyen

DATE CONSIDERED

12/28/02

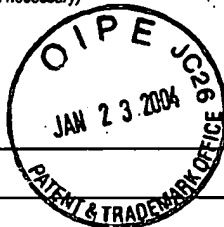
Substitute Disclosure Statement Form (PTO-1449)

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US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
W	US-4,598,305	07/01/1986	Chiang, A. , et al.	357	23.7	06/18/1984
W	US-4,980,303	12/25/1990	Yamauchi, T.	437	31	08/18/1988
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W	JP-60-024678	02/07/1985	Akio, Nakatani	G06 K	9/36	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
W		NAKAMURA, J. , et al., "CMOS Active Pixel Image Sensor with Simple Floating Gate Pixels", IEEE Transactions on Electron Devices, 42, (1995), 1693-1694	
W		WOLF, S. , Silicon Processing for the VLSI Era, Vol. 3, Lattice Press, Sunset Beach, CA, (1995), 311-312	

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Application Number	09/135413
Filing Date	August 14, 1998
First Named Inventor	Forbes, Leonard
Group Art Unit	2818-2827
Examiner Name	Nguyen, Viet

Sheet 1 of 1

Attorney Docket No: 303.354US2

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W	US-5,623,160	04/22/1997	Liberkowsky, J. B.	257	621	09/14/1995

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[A large diagonal line is drawn across the page, likely indicating that no other documents were cited.]

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